030416 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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	Re Application of e Wu) For:	TEMPERATURE COMPENSATEI VOLTAGE CONTROLLED OSCILLATOR
Ser	ial No. Unknown	į	
File	ed: February 5, 2004))) Group No.	Unknown
	INFORMATION I	DISCLOSURE S R 37 CFR § 1.97	
P.O.	nmissioner of Patents Box 1450 andria, VA 22313-1450		
Dear	· Sir:		
oubli	Applicants submit herewith, in ications and other information listed of		th 37 CFR § 1.98, the patents, orm PTO-1449.
			
	CERTIFICATE OF MAIL		• • • • • • • • • • • • • • • • • • • •
here	eby certify that this correspondence is, on the	e date shown below,	
_	MAILING		FACSIMILE
M	deposited with the United States Postal Servi with sufficient postage as first class mail, in envelope addressed to the Commissioner 1 Patents, P.O. Box 1450, Alexandria, VA 2231 1450.	an Trac for 13-	smitted by facsimile to the Patent and demark Office. or's Name: Theresa Badet (type or print name)
Date	e: February 5, 2004	Signatur	e: Meyon Bady

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The references submitted herewith are those of which Applicants are aware, which they believe may be material to the examination of this application and, with respect to which there may be a duty to disclose in accordance with 37 CFR § 1.56. While the references identified may be material to the examination of this application pursuant to 37 CFR § 1.56, the citation of these references is not intended to constitute an admission that any reference referred to herein is prior art to the invention of this application, unless specifically designated as such. Additionally, the filing of this Information Disclosure Statement shall not be construed to mean that any search has been made or, that if made, such search was complete or exhaustive, or that no other material information as defined in 37 CFR § 1.56 exists.

In accordance with 37 CFR § 1.97(b)(3), this Information Disclosure Statement is being submitted before mailing of the first Office Action on the merits.

A list of the references is set forth on Form PTO-1449, which is enclosed herewith, along with a copy of the cover page of each cited reference. If Examiner prefers a hard copy of each reference, please contact the undersigned and a full copy of the reference will be forwarded. Applicants respectfully request that the Examiner return to Applicants a copy of the Form PTO-1449 indicating consideration of the references cited thereon.

Applicants do not believe that any fees are due. If, however, it is determined that fees are owed, Applicants hereby authorize that such fees be charged to Deposit Account No. 17-0026.

Respectfully submitted,

Dated: February 5, 2004

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE	ATTY. DOCKET NO. SERIAL NO. Unknown					
STATEMENT BY APPLICANT (Use several sheets if necessary)	APPLICANT Yue Wu					
DATE MAILED: February 5, 2004	FILING DATE February 5, 2004	GROUP Unknown				

U.S. PATENT DOCUMENTS EXAMINER FILING DATE Ref CLASS INITIAL DOCUMENT NUMBER DATE NAME **SUB** IF APPRO-No CLASS PRIATE 08/21/62 D.V. Sinninger A1 A2 01/21/64 E.O. Keizer **A3** 12/10/68 F.W. Kruse, Jr., et al. 11/09/71 Klein et al. A4 5 7 0 12/24/74 Grebene Α5 02/18/75 Gili **A**6 12/30/86 Α7 Vanderspool, III 6 3 08/09/88 Haight et al. **A8** 07/25/89 Α9 **Dobos** 05/01/90 A10 Lax 12/18/90 A11 Suter A12 01/12/93 Camp, Jr., et al. 05/23/95 Glazebrook A13 10/31/95 A14 Buoli et al. 05/06/97 A15 Meyer 05/26/98 Nonaka et al. A16 08/04/98 A17 Drost et al. 06/15/99 Ma et al. A18 10/26/99 A19 Iravani et al. A20 10/23/01 Welland et al. 09/03/02 A21 Cox et al. 05/13/03 A22 Masenas et al. 06/03/03 A23 Welland et al. 10/17/02 A24 Toncich et al. 02/13/03 A25 Duncan et al.

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	Ε	юс	UMI	ENT	NUI	мве	R	DATE	COUNTRY	NAME	 CLASS	SUB CLASS
	B1	0	7	3	5	6	9	3	10/02/96	EP			
	B2	1	1	8	9	3	5	1	03/20/02	EP			
	В3	7	1	2	3	3	2	7	05/12/95	JP			•
	B4						L	_			 		
	B5												
	В6												
	В7												

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

C1	DeVito, Lawrence (2003) Unusual clock recovery architecture ameliorates SONET jitter tradeoff. Presented at University of California Berkeley.
C2	McMahill, et al. (2002) Automatic calibration of modulated frequency synthesizers. IEEE Transactions on Circuits and Systems 49(5):301-311.
EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.